

DrM Transmitter With Fpga Device Radioeng

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DRM Transmitter with FPGA Device - Radioeng

DRM Transmitter with FPGA Device Pavel DOBIÁŠ Dept of Radio Electronics, Czech Technical University, Technická 2, 166 27 Praha, Czech Republic dobiasp@fel.cvut.cz Abstract This paper presents the design and analysis of the OFDM modulator for Digital Radio Mondiale (DRM) standard Attention is paid to the digital signal processing in FPGA

Specifications TSW 2100 100 kW Shortwave Transmitter

Specifications TSW 2100 100 kW Shortwave Transmitter DRM Digital Analog AM Modulation DRM standard (ETSI ES 201 980) DSB: A3E (option A3E with DCC or AMC) the latest embedded PC and FPGA technology The in - Digital Radio Mondiale (DRM) DRM ...

D A T A S H E E T S-Band DTRM - Mistral Solutions

The Transmitter Card consists of a Pre-Driver, GaN power Amplifier and Power Detector Sections The Power Detector Section is used to detect the transmitter power level and can be monitored by FPGA The Digital card consists of a Power supply card and a Smart-Fusion FPGA from Actel It is configured to receive data from host controller and

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500 kW Shortwave Transmitter - AMPEGON

power shortwave transmitter TSW product line ext-ends from 50 kW to 500 kW As a proud founding member of the Digital Radio Mondiale (DRM) Consortium, Ampegon strongly sup-ports the digital broadcasting system Therefore, the Ampegon TSW product line is fully DRM compatibl and

enables broadcasters to choose between classical AM

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NX5 AM Transmitter - Nautel Broadcast

ing FPGA/DSP technology with automatic changeover Pre-correction utilized specifically to improve digital performance RF Output Power Carrier Power Range: 0 to 5500 W Up to 62 preset levels, presets may include other operational parameters such as DRM vs Analog and different program inputs Output level stabilized against AC supply voltage

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