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Chapter 7 Memory and Programmable Logic

n The equivalent logic of a binary cell that stores one bit of information is shown below Read/Write = 0, select = 1, input data to S-R latch Read/Write = 1, select = 1, output data from S-R latch SR latch with NOR gates Ref Figure 5-3 18 4X4 RAM n There is a need for decoding

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